

DAFTAR GAMBAR

GAMBAR 1 JANIN	13
GAMBAR 2 FETAL DOPPLER	14
GAMBAR 3 SENSOR TRANDUSER	16
GAMBAR 4 SINYAL PCG NORMAL [6]	19
GAMBAR 5 CONTOH SINYAL SUARA JANTUNG JANIN NORMAL (A) ABNORMAL (B) [5]	20
GAMBAR 6 ARDUINO UNO	20
GAMBAR 7 KONFIGURASI PIN ARDUINO UNO	22
GAMBAR 8 4,3 INCH LCD TFT	25
GAMBAR 9 HIGH PASS FILTER ORDE-2	28
GAMBAR 10 LOW PASS FILTER ORDE-2	29
GAMBAR 11 <i>NOTCH FILTER</i>	29
GAMBAR 12 DIAGRAM BLOK SISTEM KESELURUHAN	31
GAMBAR 13 DIAGRAM ALIR	33
GAMBAR 14 DESIGN ALAT	34
GAMBAR 15 OUTPUT SENSOR TRANDUSER FETAL DOPPLER	43
GAMBAR 16 OUTPUT TRANDUSER FETAL DOPPLER SETELAH FILTER	44
GAMBAR 17 RESPON FREKUENSI RANGKAIAN FILTER	44
GAMBAR 18 OUTPUT NOTCH FILTER	45
GAMBAR 19 RESPON FREKUENSI NOTCH FILTER	46
GAMBAR 20 OUTPUT PRE-AMP	46
GAMBAR 21 OUTPUT <i>SUMMING AMPLIFIER</i>	47
GAMBAR 22 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 60	48
GAMBAR 23 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 90	49
GAMBAR 24 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 120	50
GAMBAR 25 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 150	52

GAMBAR 26 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 180	53
GAMBAR 27 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 60	55
GAMBAR 28 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 90	56
GAMBAR 29 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 120	57
GAMBAR 30 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR BPM 150	59
GAMBAR 31 TAMPILAN LCD DAN FETAL DOPPLER SIMULATOR	60
GAMBAR 32 RANGKAIAN <i>HIGH PASS FILTER</i>	63
GAMBAR 33 RANGKAIAN LOW PASS FILTER	64
GAMBAR 34 RANGKAIAN <i>NOTCH FILTER</i>	65